

What is claimed is:

1. A semiconductor device for output interface having an interface function with an output device, the semiconductor device comprising:

an input terminal to which compressed data is input;  
a decompression section which decompresses the compressed data; and

an output terminal for outputting data decompressed by the decompression section to the output device.

2. A semiconductor device for input interface having an interface function with an input device, the semiconductor device comprising:

an input terminal to which uncompressed data is input from the input device;

a compression section which compresses the uncompressed data; and

an output terminal for outputting data compressed by the compression section.

3. A semiconductor device for driving a display section, the semiconductor device comprising:

an input terminal to which compressed data is input;  
a decompression section which decompresses the compressed data; and

an output terminal for outputting data decompressed by

*S'0 A1* | the decompression section to the display section.

4. The semiconductor device according to claim 1,  
wherein the compressed data input to the input terminal  
5 is compressed data demultiplexed from multiplexed data which  
is multiplexed one or more types of compressed data, and  
wherein the decompression section decompresses the  
demultiplexed and compressed data.

10 5. The semiconductor device according to claim 2,  
wherein the compressed data output from the output  
terminal is multiplexed together with one or more types of  
compressed data.

15 6. The semiconductor device according to claim 3,  
wherein the compressed data input to the input terminal  
is compressed data demultiplexed from multiplexed data which  
is multiplexed one or more types of compressed data, and  
wherein the decompression section decompresses the  
20 demultiplexed and compressed data.

7. The semiconductor device according to claim 1,  
wherein the compressed data is compressed according to  
a given compression standard.

25 8. The semiconductor device according to claim 2,  
wherein the compressed data is compressed according to

a given compression standard.

9. The semiconductor device according to claim 3,  
wherein the compressed data is compressed according to  
5 a given compression standard.

10. The semiconductor device according to claim 4,  
wherein the compressed data is compressed according to  
a given compression standard.

10 11. The semiconductor device according to claim 5,  
wherein the compressed data is compressed according to  
a given compression standard.

15 12. The semiconductor device according to claim 6,  
wherein the compressed data is compressed according to  
a given compression standard.

13. The semiconductor device according to claim 7,  
20 wherein the given compression standard is an MPEG  
standard.

14. The semiconductor device according to claim 8,  
wherein the given compression standard is an MPEG  
25 standard.

15. The semiconductor device according to claim 9,

wherein the given compression standard is an MPEG standard.

16. Electronic equipment comprising:

5       the semiconductor device according to claim 1; and  
a demultiplexing section which demultiplexes compressed  
data corresponding to the semiconductor device from multiplexed  
data which is multiplexed one or more types of compressed data,  
and supplies the demultiplexed and compressed data to the  
10      semiconductor device.

17. Electronic equipment comprising:

the semiconductor device according to claim 2; and  
a multiplexing section which generates multiplexed data  
15      by multiplexing one or more types of compressed data including  
compressed data supplied from the semiconductor device.

18. Electronic equipment comprising:

the semiconductor device according to claim 3;  
20       a demultiplexing section which demultiplexes compressed  
data corresponding to the semiconductor device from multiplexed  
data which is multiplexed one or more types of compressed data,  
and supplies the demultiplexed and compressed data to the  
semiconductor device; and  
25       a display section which is driven by the semiconductor  
device.

19. The electronic equipment according to claim 16,  
further comprising:

a circuit which transmits and receives the multiplexed  
data through a given communication network.

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20. The electronic equipment according to claim 17,  
further comprising:

a circuit which transmits and receives the multiplexed  
data through a given communication network.

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21. The electronic equipment according to claim 18,  
further comprising:

a circuit which transmits and receives the multiplexed  
data through a given communication network.

ADD A2 >